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blocks for new circuit  
technology, integrated  
circuit testing and fault  
diagnosis, digital signal  
processing, computer-aided  
synthesis, field  
programmable gate arrays  
(FPGAs), and design of low-  
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Realizations of  
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For Spring 2015, the course syllabus was changed with the integration of industrial VLSI grade CAD using Synopsys. Previously, simulations were limited and performed with open source software. With Synopsys, students used methodology similar to the process used in industry to design

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## *Incorporating Synopsys CAD Tools In Teaching VLSI Design*

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*Computer Aids for VLSI  
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Digital Integrated Circuits

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## AND VLSI Fundamentals

Course: ESE570 Units: 1.0 CU

Term: Fall 2020 When: MW

4:30-6pm EDT (note this will  
change to EST on Nov. 1)

Where: Zoom, see Piazza for  
link Instructor: Tania

Khanna (Levine 262, seas:

taniak) (office hours: W

1-3pm, F 9-10am EDT and by

appointment, see Piazza for  
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(seas: ylxiao) (office

hours: Th 9-10:30am, F

2-3:30pm ...

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